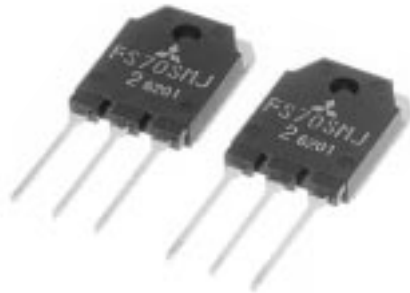


# FS70SMJ-2

HIGH-SPEED SWITCHING USE

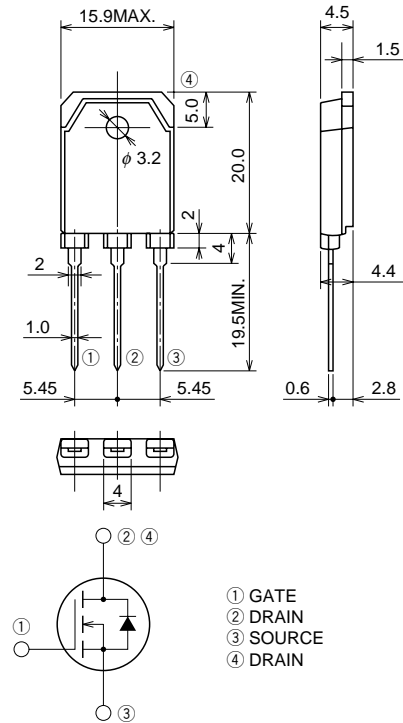
## FS70SMJ-2



- 4V DRIVE
- $V_{DS}$  ..... 100V
- $r_{DS(ON)}$  (MAX) ..... 17m $\Omega$
- $I_D$  ..... 70A
- Integrated Fast Recovery Diode (TYP.) ..... 115ns

## OUTLINE DRAWING

Dimensions in mm



TO-3P

## APPLICATION

Motor control, Lamp control, Solenoid control  
DC-DC converter, etc.

## MAXIMUM RATINGS (Tc = 25°C)

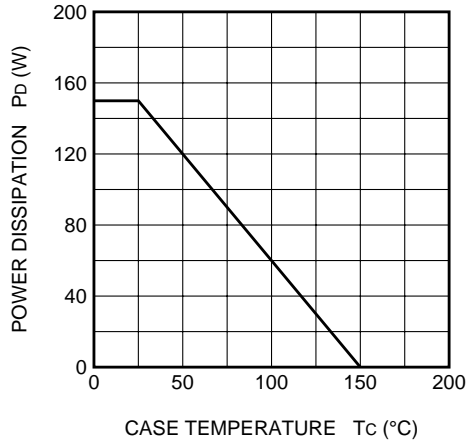
Symbol	Parameter	Conditions	Ratings	Unit
$V_{DS}$	Drain-source voltage	$V_{GS} = 0V$	100	V
$V_{GS}$	Gate-source voltage	$V_{DS} = 0V$	$\pm 20$	V
$I_D$	Drain current		70	A
$I_{DM}$	Drain current (Pulsed)		280	A
$I_{DA}$	Avalanche drain current (Pulsed)	$L = 100\mu H$	70	A
$I_S$	Source current		70	A
$I_{SM}$	Source current (Pulsed)		280	A
$P_D$	Maximum power dissipation		150	W
$T_{ch}$	Channel temperature		-55 ~ +150	°C
$T_{stg}$	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	4.8	g

**ELECTRICAL CHARACTERISTICS** ( $T_{ch} = 25^{\circ}\text{C}$ )

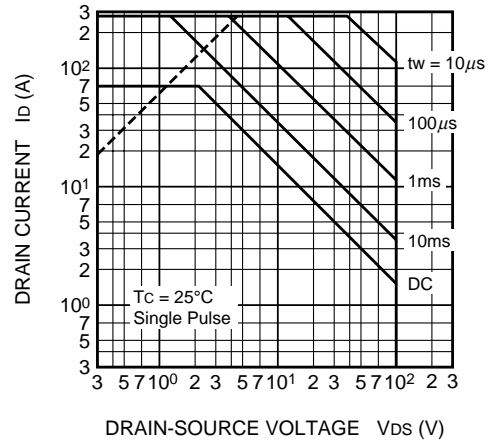
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-source breakdown voltage	$I_D = 1\text{mA}$ , $V_{GS} = 0\text{V}$	100	—	—	V
$I_{GSS}$	Gate-source leakage current	$V_{GS} = \pm 20\text{V}$ , $V_{DS} = 0\text{V}$	—	—	$\pm 0.1$	$\mu\text{A}$
$I_{DSS}$	Drain-source leakage current	$V_{DS} = 100\text{V}$ , $V_{GS} = 0\text{V}$	—	—	0.1	mA
$V_{GS(th)}$	Gate-source threshold voltage	$I_D = 1\text{mA}$ , $V_{DS} = 10\text{V}$	1.0	1.5	2.0	V
$r_{DS(on)}$	Drain-source on-state resistance	$I_D = 35\text{A}$ , $V_{GS} = 10\text{V}$	—	13	17	$\text{m}\Omega$
$r_{DS(on)}$	Drain-source on-state resistance	$I_D = 35\text{A}$ , $V_{GS} = 4\text{V}$	—	14	18	$\text{m}\Omega$
$V_{DS(on)}$	Drain-source on-state voltage	$I_D = 35\text{A}$ , $V_{GS} = 10\text{V}$	—	0.46	0.60	V
$ y_{fs} $	Forward transfer admittance	$I_D = 35\text{A}$ , $V_{DS} = 10\text{V}$	—	68	—	S
$C_{iss}$	Input capacitance	$V_{DS} = 10\text{V}$ , $V_{GS} = 0\text{V}$ , $f = 1\text{MHz}$	—	8200	—	pF
$C_{oss}$	Output capacitance		—	1150	—	pF
$C_{rss}$	Reverse transfer capacitance		—	600	—	pF
$t_d(on)$	Turn-on delay time	$V_{DD} = 50\text{V}$ , $I_D = 35\text{A}$ , $V_{GS} = 10\text{V}$ , $R_{GEN} = R_{GS} = 50\Omega$	—	54	—	ns
$t_r$	Rise time		—	140	—	ns
$t_d(off)$	Turn-off delay time		—	830	—	ns
$t_f$	Fall time		—	350	—	ns
$V_{SD}$	Source-drain voltage	$I_S = 35\text{A}$ , $V_{GS} = 0\text{V}$	—	1.0	1.5	V
$R_{th(ch-c)}$	Thermal resistance	Channel to case	—	—	0.83	$^{\circ}\text{C/W}$
$t_{rr}$	Reverse recovery time	$I_S = 70\text{A}$ , $di/dt = -100\text{A}/\mu\text{s}$	—	115	—	ns

**PERFORMANCE CURVES**

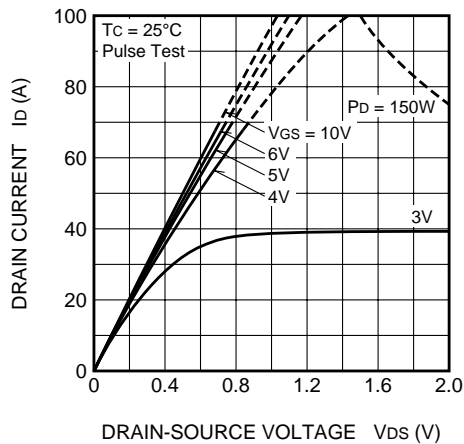
**POWER DISSIPATION DERATING CURVE**



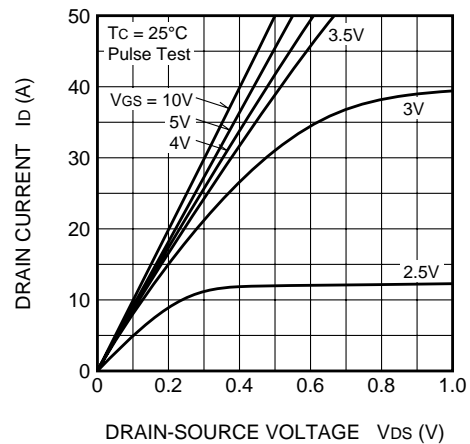
**MAXIMUM SAFE OPERATING AREA**

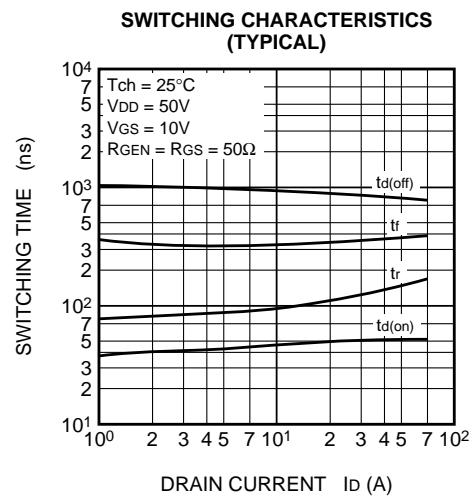
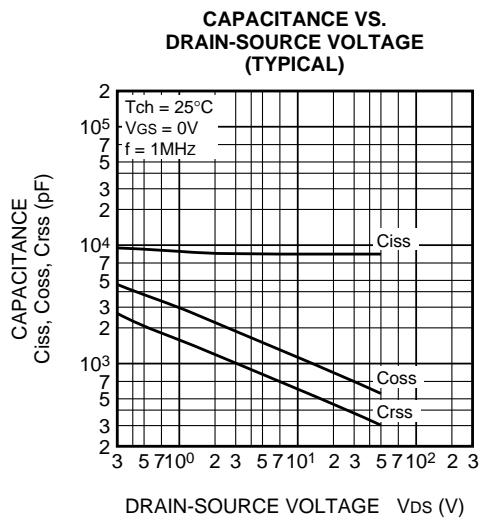
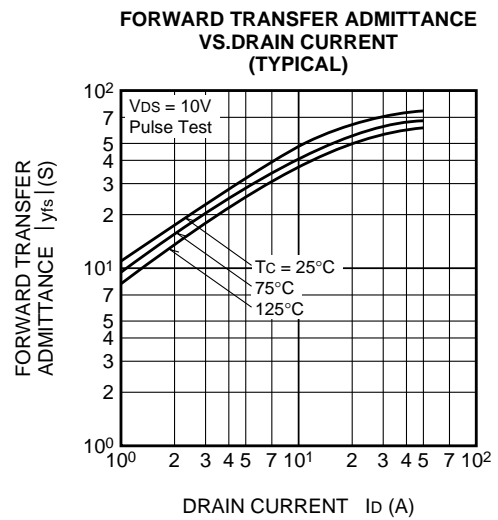
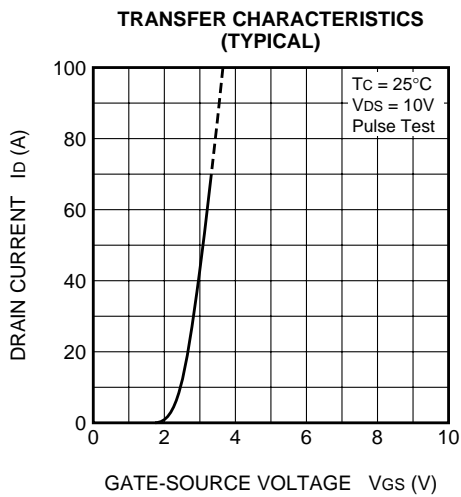
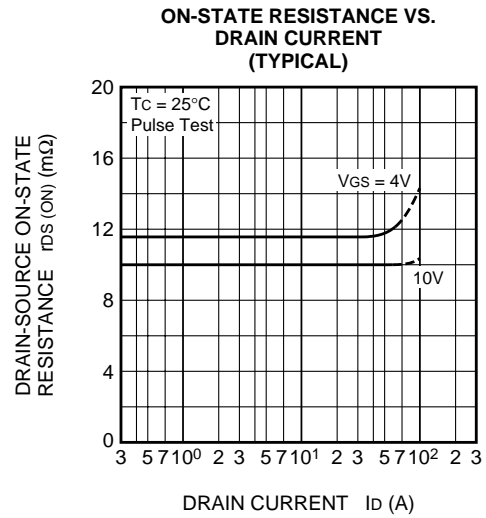
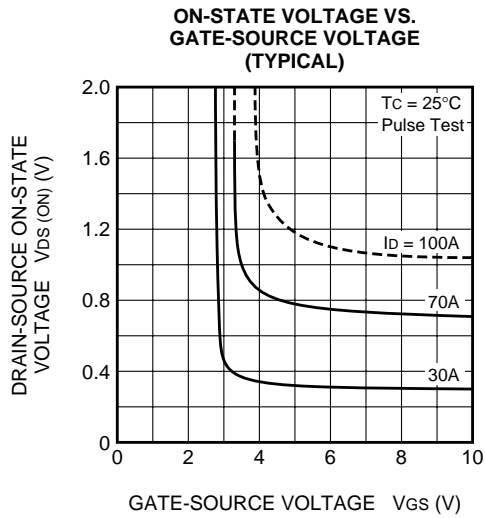


**OUTPUT CHARACTERISTICS (TYPICAL)**

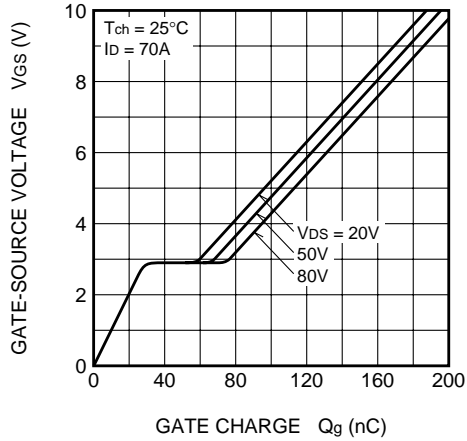


**OUTPUT CHARACTERISTICS (TYPICAL)**

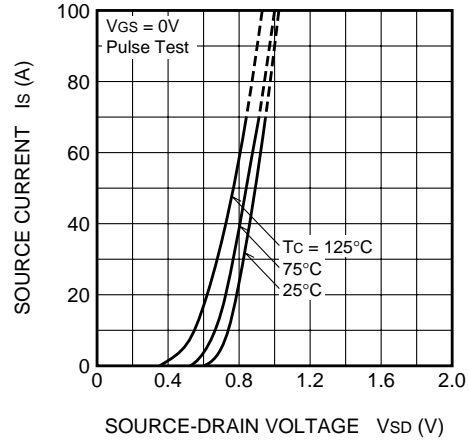




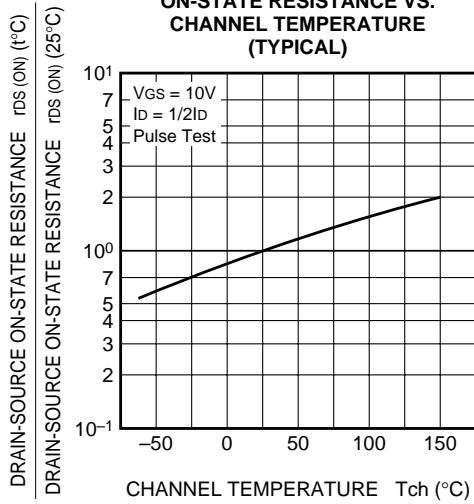
GATE-SOURCE VOLTAGE  
VS. GATE CHARGE  
(TYPICAL)



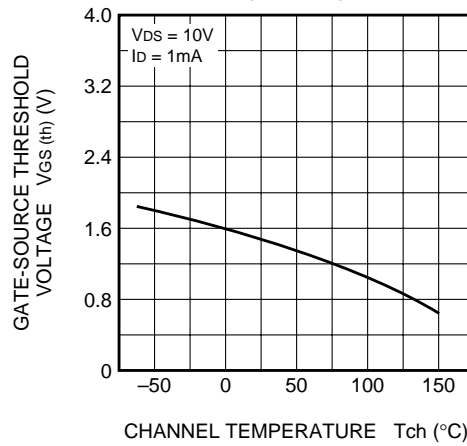
SOURCE-DRAIN DIODE  
FORWARD CHARACTERISTICS  
(TYPICAL)



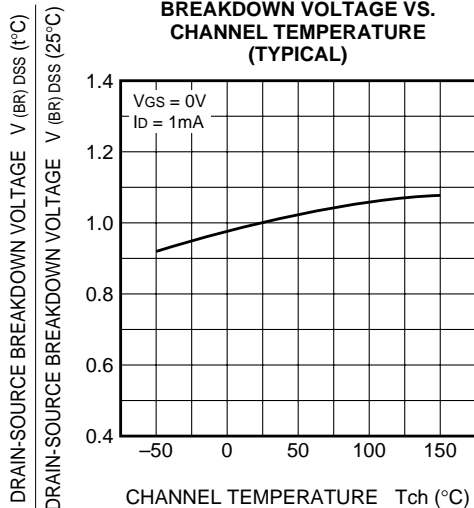
ON-STATE RESISTANCE VS.  
CHANNEL TEMPERATURE  
(TYPICAL)



THRESHOLD VOLTAGE VS.  
CHANNEL TEMPERATURE  
(TYPICAL)



BREAKDOWN VOLTAGE VS.  
CHANNEL TEMPERATURE  
(TYPICAL)



TRANSIENT THERMAL IMPEDANCE  
CHARACTERISTICS

